

FIG. 1a

FIG. 1b

Register Region								Value		Mask	
Action	Primitive Operation	Reserved	Size	ID	Width	Offset	Reserved	Address 0	Address 1	Address 2	Address 3
0x09	0x82	0	0x04	0x02	0x04	0x00	0x00	0x0000	0x0007	0x0003	0x006c

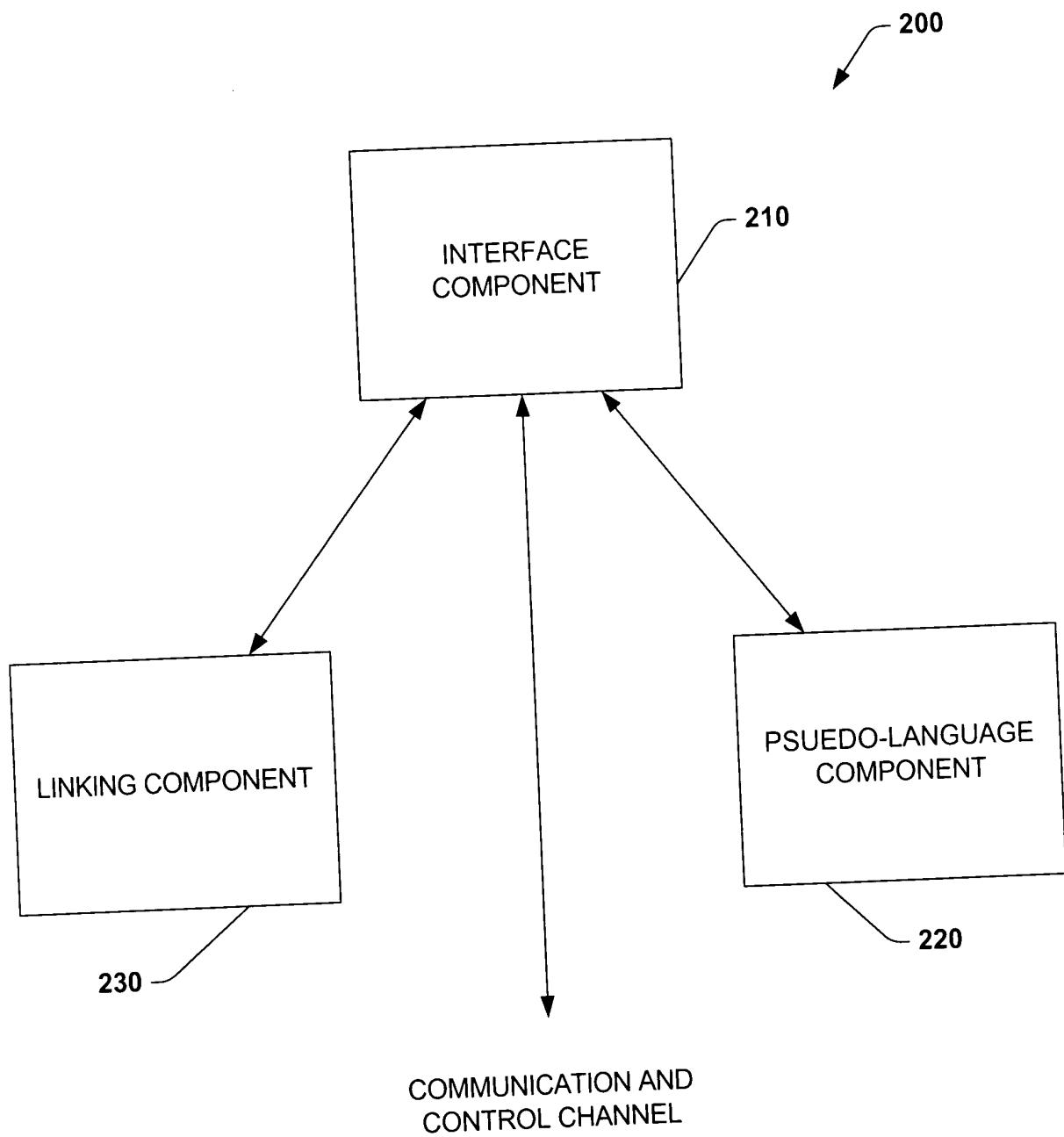


FIG. 2

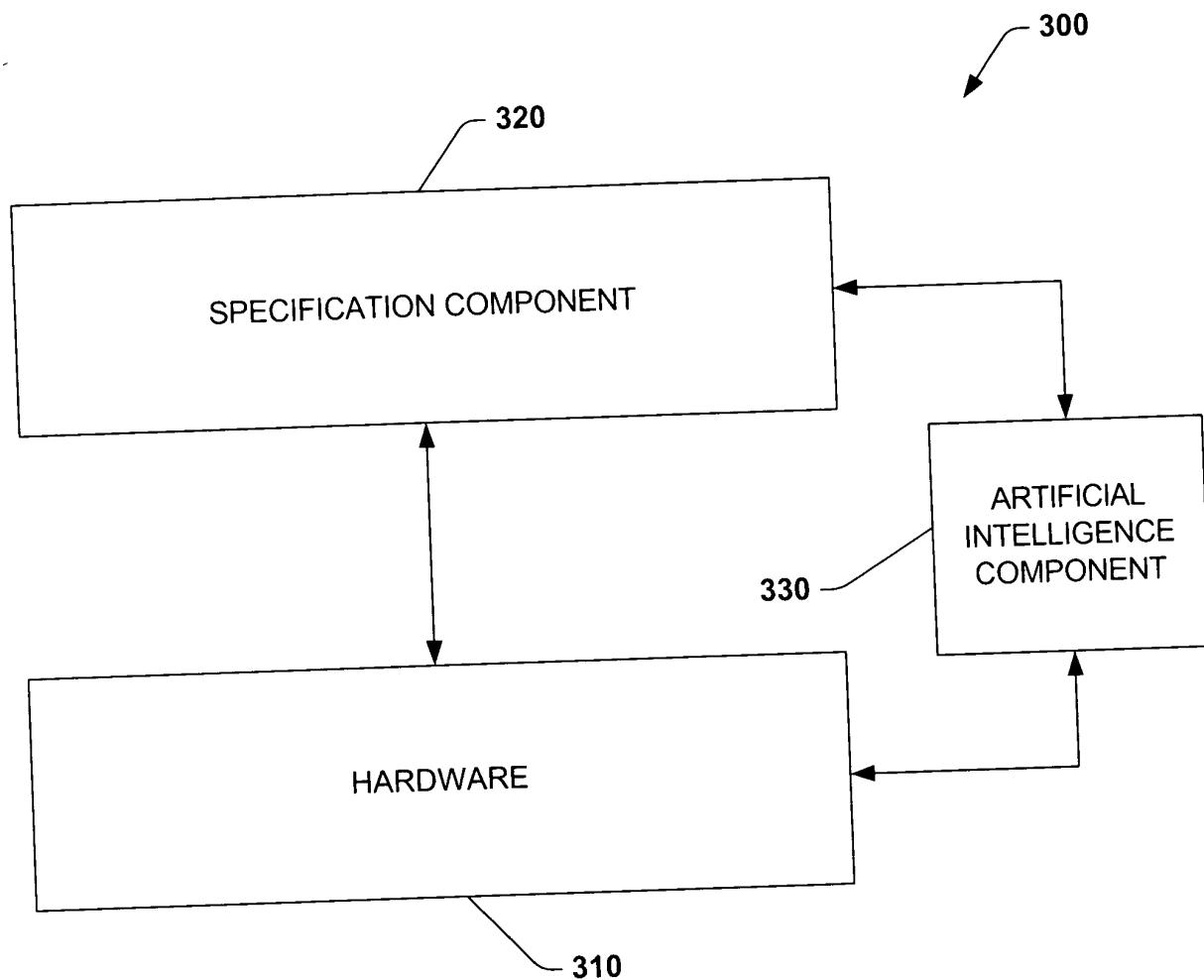


FIG. 3

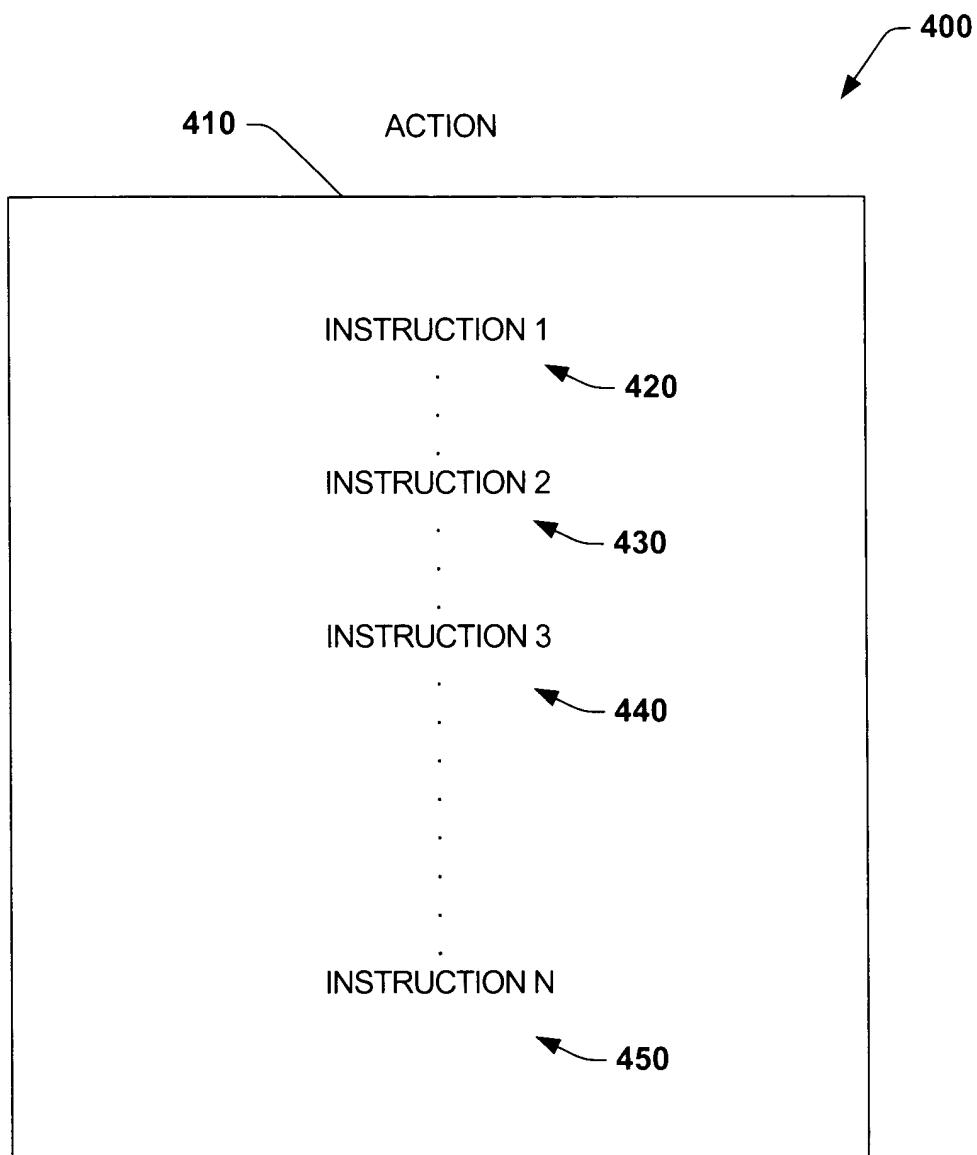


FIG. 4

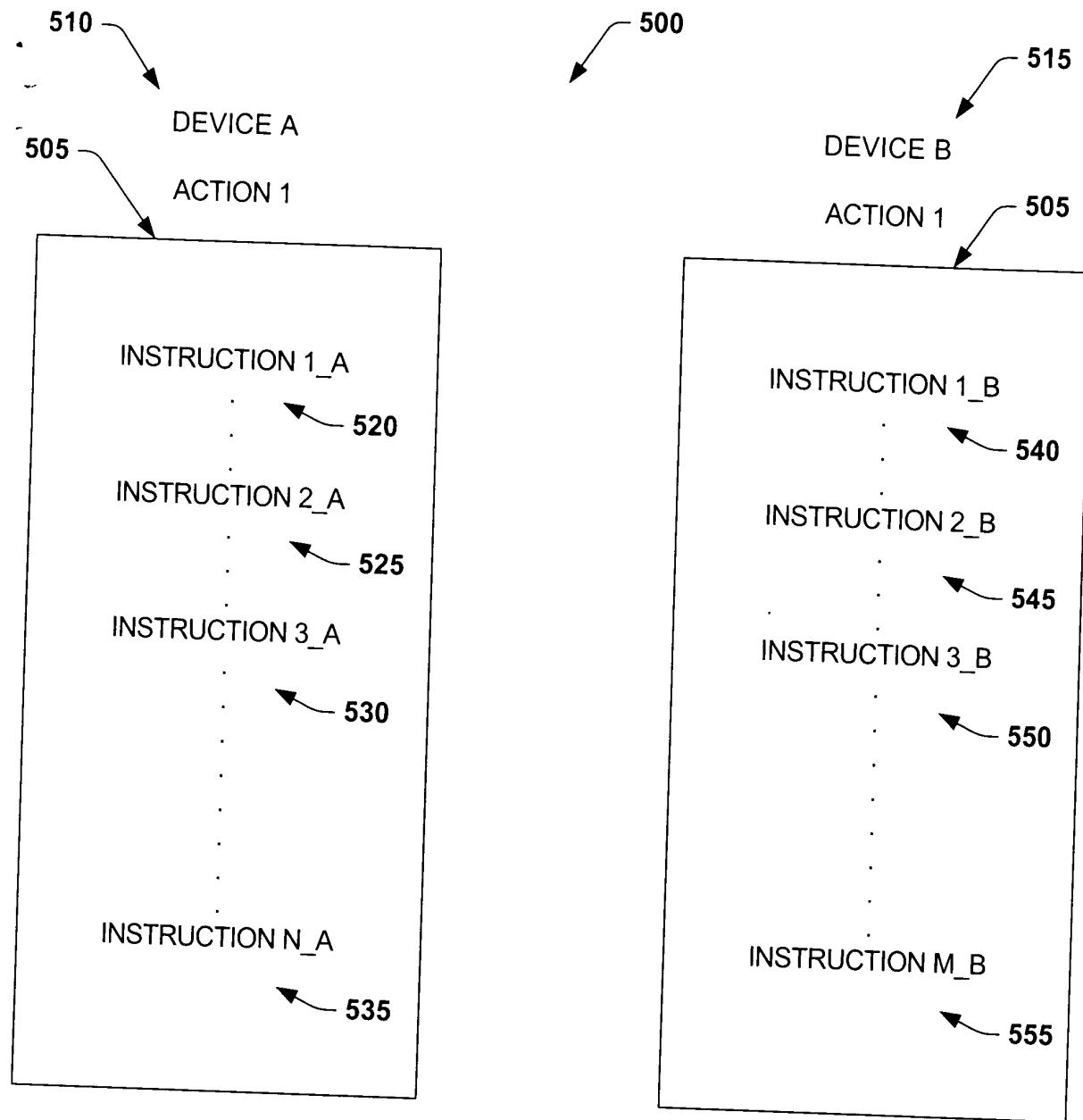


FIG. 5

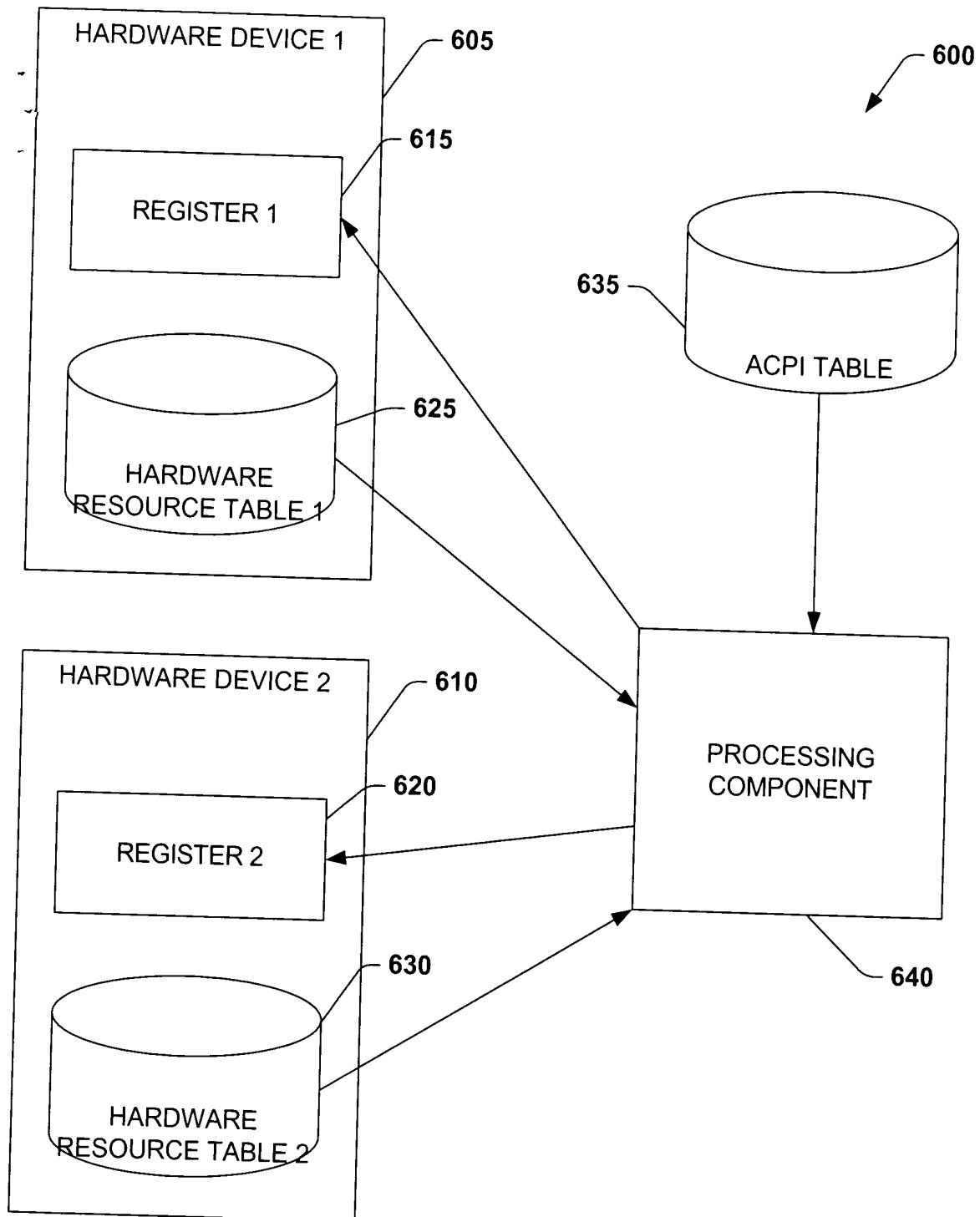


FIG. 6

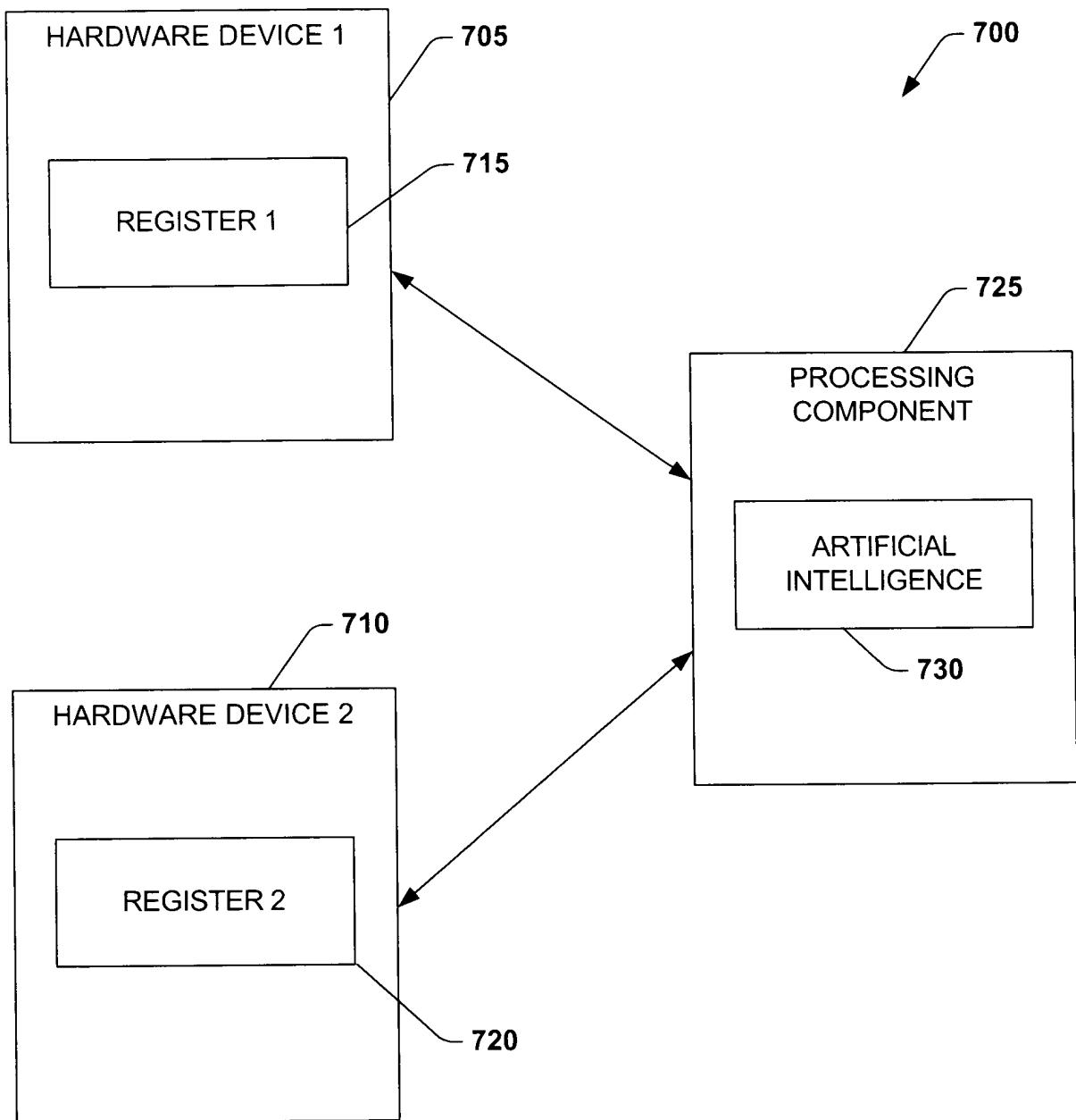


FIG. 7

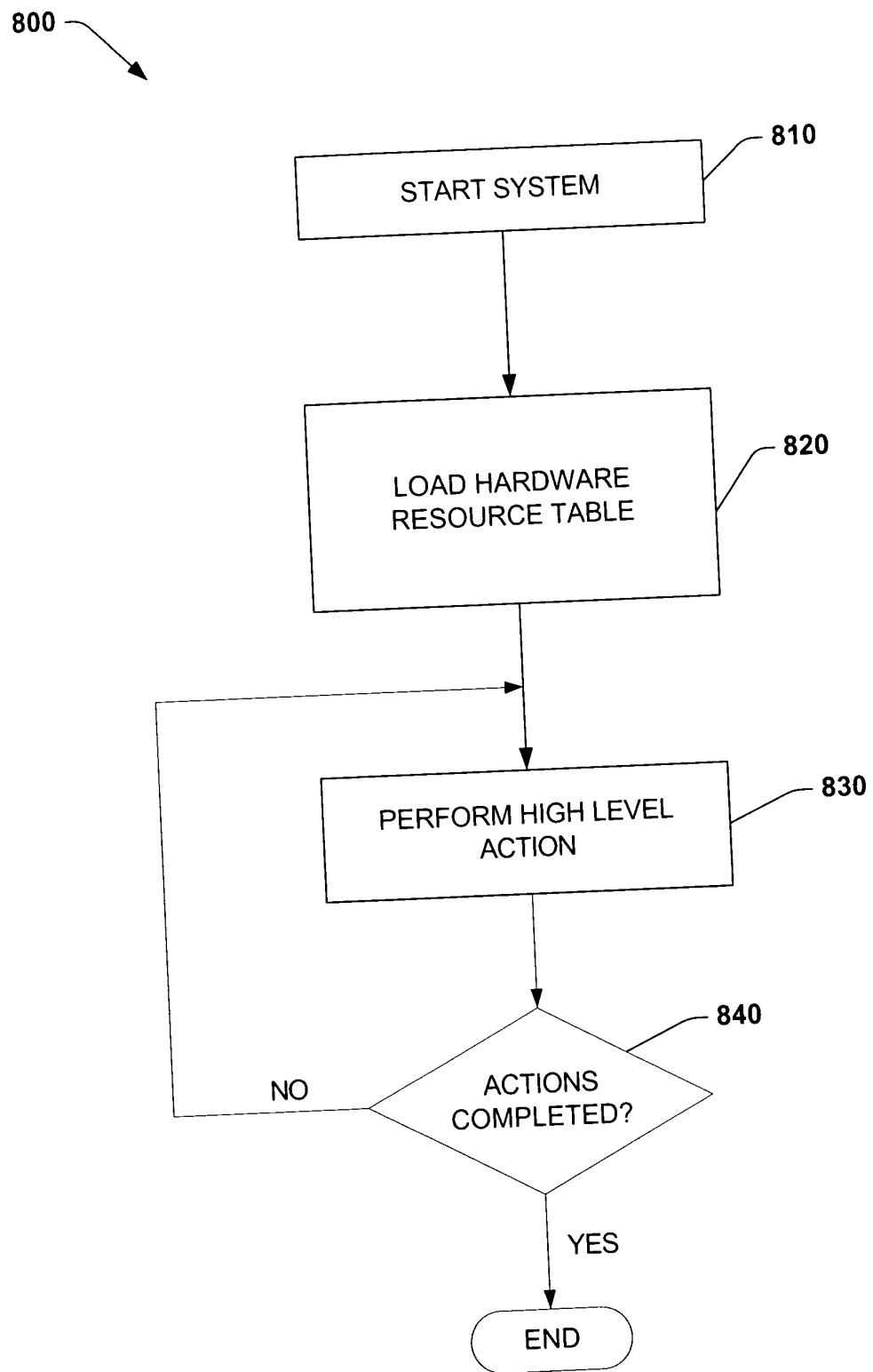


FIG. 8

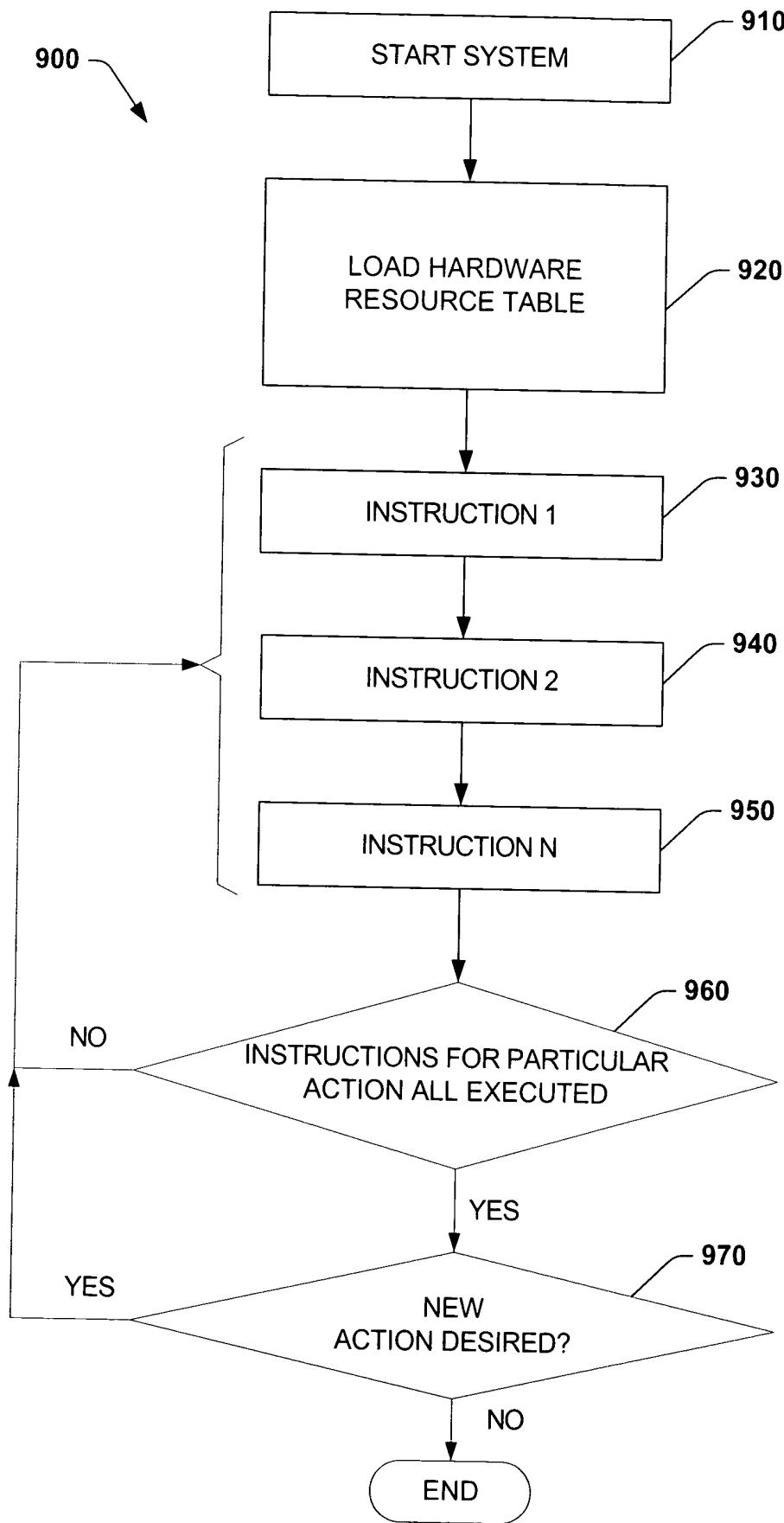


FIG. 9

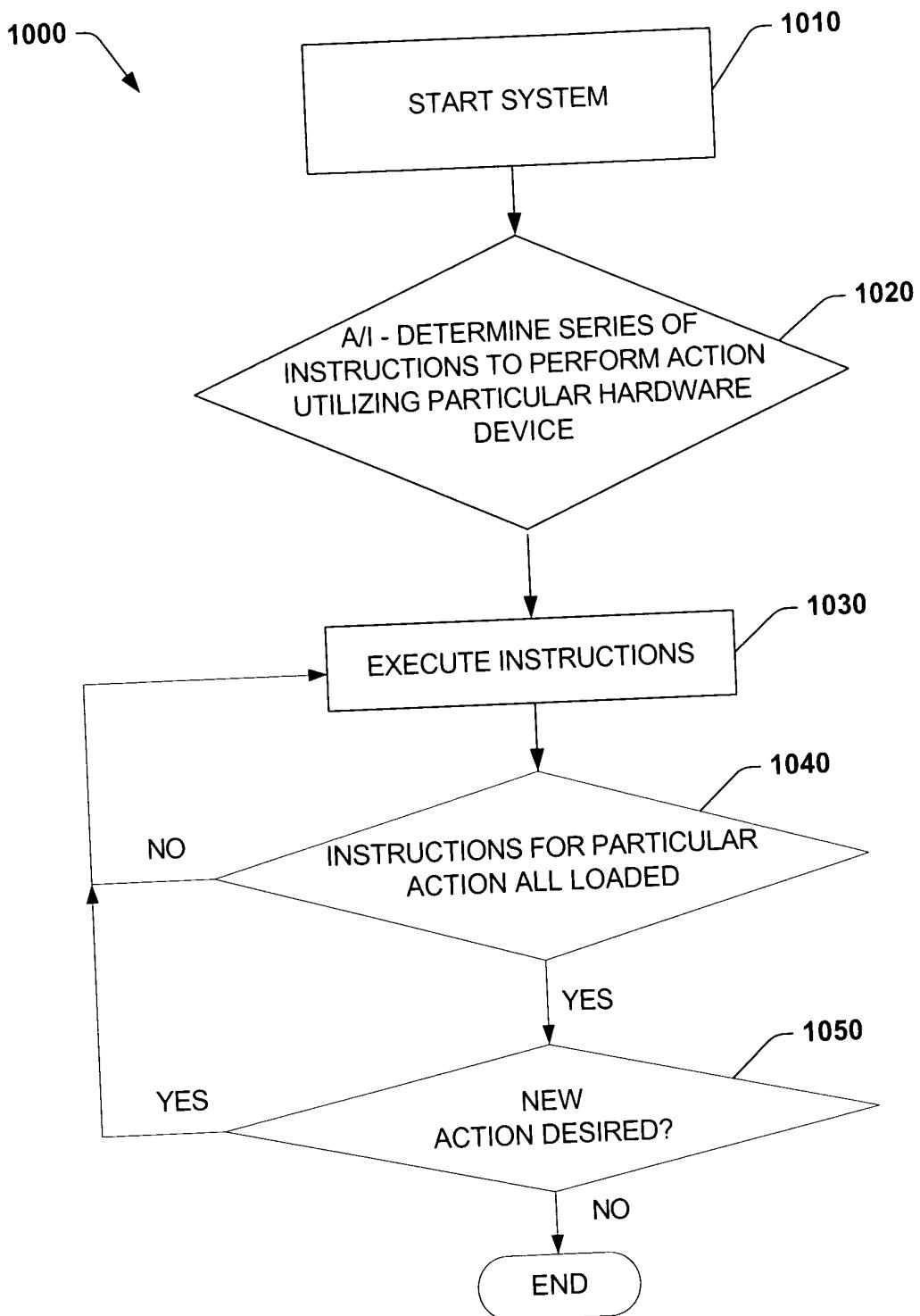


FIG. 10

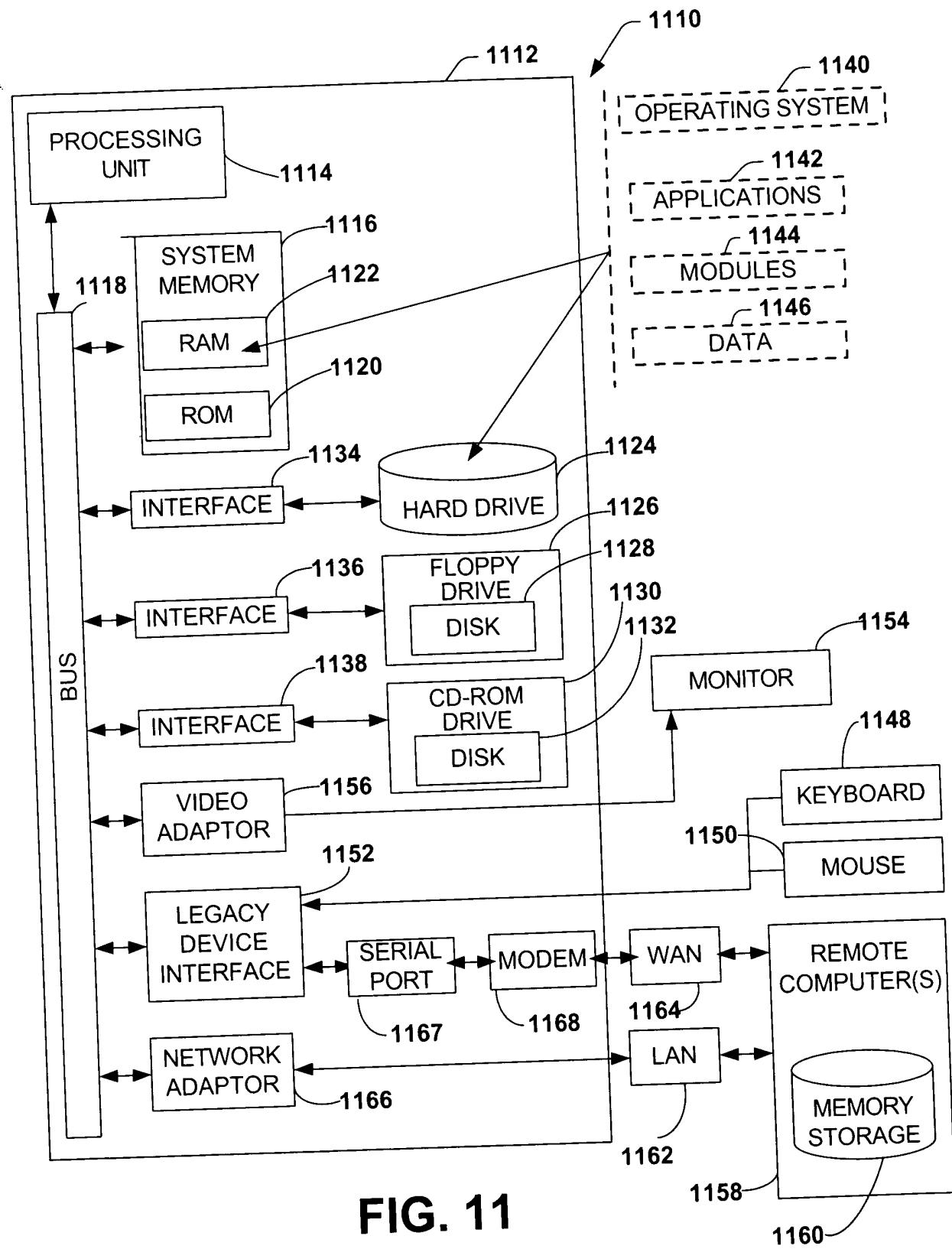


FIG. 11

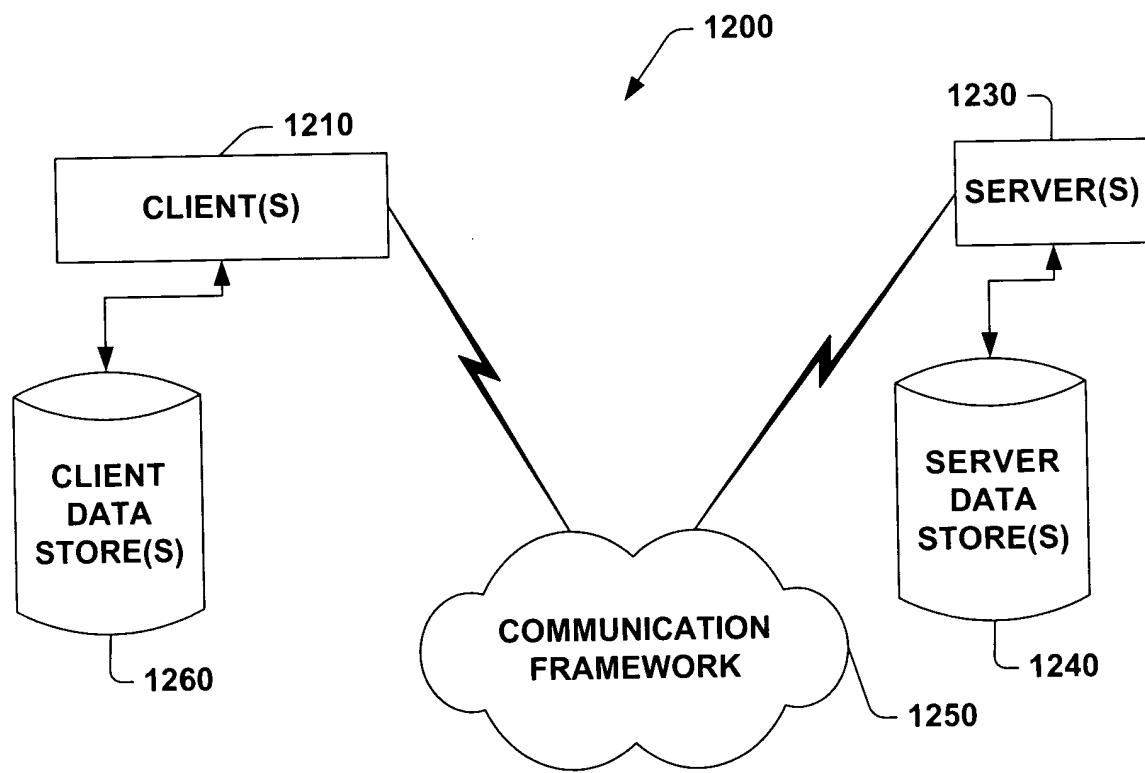


FIG. 12